

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	225	(marking near3 defect\$3)and(wafer\$1 or semiconductor\$1 or IC or intergrated adj circuit)	USPAT	OR	ON	2005/11/17 12:03
L2	23	L1 and inspect\$3 and(photomask\$3 or reticle)	USPAT	OR	ON	2005/11/17 12:08
L3	0	L2 and (dispos\$4 or eliminat\$3)near3 defect and finder near3 mark	USPAT	OR	ON	2005/11/17 12:12
L4	627	382/144,149.ccls.	USPAT	OR	ON	2005/11/17 12:12
L5	0	356/237.1.1ccls.	USPAT	OR	ON	2005/11/17 12:13
L6	748	356/237.1.ccls.	USPAT	OR	ON	2005/11/17 12:13
L7	701	427/140.ccls.	USPAT	OR	ON	2005/11/17 12:13
L8	1	L2 and L4	USPAT	OR	ON	2005/11/17 12:17
L9	0	L8 and @ad<"20011116"	USPAT	OR	ON	2005/11/17 12:15
L10	1	L2 and L6	USPAT	OR	ON	2005/11/17 12:20
L11	0	L2 and L7	USPAT	OR	ON	2005/11/17 12:20
L12	236	250/559.44.ccls.	USPAT	OR	ON	2005/11/17 12:21
L13	0	L2 and L12	USPAT	OR	ON	2005/11/17 12:21

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(marking near3 defect\$3 and(wafer\$1 or semiconductor\$1 or IC or intergrated adj circuit)and inspect\$3 and(photomask\$3 or reticle)and(dispos\$4 or eliminat\$3)and defect and finder near3 mark).clm.	US-PGPUB	OR	ON	2005/11/17 13:00

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3	INZZ	L1 AND repair\$3 AND (photomask OR reticle) AND (wafer\$1 OR semiconductor\$1 OR chip\$1 OR IC OR integrated ADJ circuit)	unrestricted	0	-
4	INZZ	L1 AND (photomask OR reticle) AND defect AND finder AND mark AND installer	unrestricted	0	-
5	INZZ	L1 AND (photomask OR reticle) AND (disposing OR eliminating) AND defect AND finder AND mark AND installer	unrestricted	0	-

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- ☒ Classification codes A: Physics, 0-1
- ☒ Classification codes A: Physics, 2-3

Day : Thursday
Date: 11/17/2005


PALM INTRANET

Time: 13:02:15

Inventor Name Search Result

Your Search was:

Last Name = HUNG

First Name = CHANG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09991341	Not Issued	71	11/16/2001	Method for applying a defect finder mark to a backend photomask making process	HUNG, CHANG CHENG
09557397	6365303	150	04/24/2000	Electrostatic discharge damage prevention method on masks	HUNG, CHANG-CHENG
09927929	6653029	150	08/10/2001	DUAL-FOCUSED ION BEAMS FOR SEMICONDUCTOR IMAGE SCANNING AND MASK REPAIR	HUNG, CHANG-CHENG
10187675	6861179	150	07/02/2002	CHARGE EFFECT AND ELECTROSTATIC DAMAGE PREVENTION METHOD ON PHOTO-MASK	HUNG, CHANG-CHENG
10196974	6858353	150	07/17/2002	INCREASED-CONTRAST FILM FOR HIGH-TRANSMITTANCE ATTENUATED PHASE-SHAFT MASKS	HUNG, CHANG-CHENG
10255482	6858354	150	09/26/2002	METHOD TO PREVENT SIDE LOBE ON SEAL RING	HUNG, CHANG-CHENG
10326499	Not Issued	30	12/20/2002	Progressive self-learning defect review and classification method	HUNG, CHANG-CHENG
10781107	Not Issued	30	02/18/2004	System and method for inspecting errors on a wafer	HUNG, CHANG-CHENG
11204691	Not Issued	30	08/16/2005	System and method for removing particles in semiconductor manufacturing	HUNG, CHANG-CHENG
11244601	Not Issued	18	10/05/2005	Dimension monitoring method and system	HUNG, CHANG-CHENG
60450177	Not Issued	159	02/26/2003	Method and system for inspecting defects in semiconductor manufacturing processes	HUNG, CHANG-CHENG
09458083	6296554	150	12/10/1999	NON-CIRCULAR WORKPIECE CARRIER	HUNG, CHANG-KU

<u>10613397</u>	Not Issued	160	07/03/2003	Laser projection display and illumination device with MEMS scanning mirror for indoor and outdoor applications	HUNG, CHANG-LI
<u>10648551</u>	Not Issued	168	08/25/2003	MEMS scanning mirror with distributed hinges and multiple support attachments	HUNG, CHANG-LI
<u>10793877</u>	Not Issued	41	03/08/2004	Micromechanical actuator with multiple-plane comb electrodes and methods of making	HUNG, CHANG-LI
<u>11224119</u>	Not Issued	20	09/13/2005	Micromechanical actuator with multiple-plane comb electrodes and methods of making	HUNG, CHANG-LI
<u>11037010</u>	Not Issued	20	01/19/2005	Multimedia speaker headphone	HUNG, CHANG-NING
<u>09159824</u>	Not Issued	161	09/23/1998	MOBILE TELEPHONE SET FOR STEERING WHEEL ON AUTOMOBILE	HUNG, CHANG-WU
<u>09901500</u>	Not Issued	161	07/03/2001	Method of laminating copper foil onto a printed circuit board	HUNG, CHANG-YUN
<u>10033755</u>	Not Issued	161	12/19/2001	Method of fabricating passive device on printed circuit board	HUNG, CHANG-YUN
<u>11015273</u>	Not Issued	20	12/16/2004	Method of laminating copper foil onto a printed circuit board	HUNG, CHANG-YUN
<u>08821217</u>	<u>6335802</u>	150	03/20/1997	IMAGE READING HEAD	HUNG-CHE, CHANG

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="HUNG"/>	<input type="text" value="CHANG"/>	<input type="button" value="Search"/>

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Time: 13:02:31

 **PALM INTRANET**

Inventor Name Search Result

Your Search was:

Last Name = LIN

First Name = CHUAN-YUAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09927929	6653029	150	08/10/2001	DUAL-FOCUSED ION BEAMS FOR SEMICONDUCTOR IMAGE SCANNING AND MASK REPAIR	LIN, CHUAN-YUAN
09991341	Not Issued	71	11/16/2001	Method for applying a defect finder mark to a backend photomask making process	LIN, CHUAN-YUAN
10326499	Not Issued	30	12/20/2002	Progressive self-learning defect review and classification method	LIN, CHUAN-YUAN
29078388	Not Issued	161	11/04/1997	SPIKED PLATE FOR A GOLF SHOE	LIN, CHUAN-YUAN

Inventor Search Completed: No Records to Display.

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Time: 13:02:44

Inventor Name Search Result

Your Search was:

Last Name = HSU

First Name = TYNG-HAO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09164043	6127075	150	10/01/1998	METHOD FOR CHECKING ACCURACY OF A MEASURING INSTRUMENT FOR OVERLAY REGISTRATION	HSU, TYNG-HAO
09991341	Not Issued	71	11/16/2001	Method for applying a defect finder mark to a backend photomask making process	HSU, TYNG-HAO
10080323	6721939	150	02/19/2002	ELECTRON BEAM SHOT LINEARITY MONITORING	HSU, TYNG-HAO
10196974	6858353	150	07/17/2002	INCREASED-CONTRAST FILM FOR HIGH-TRANSMITTANCE ATTENUATED PHASE-SHAFT MASKS	HSU, TYNG-HAO
10326499	Not Issued	30	12/20/2002	Progressive self-learning defect review and classification method	HSU, TYNG-HAO
10781107	Not Issued	30	02/18/2004	System and method for inspecting errors on a wafer	HSU, TYNG-HAO
60450177	Not Issued	159	02/26/2003	Method and system for inspecting defects in semiconductor manufacturing processes	HSU, TYNG-HAO

Inventor Search Completed: No Records to Display.

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Time: 13:03:11

Inventor Name Search Result

Your Search was:

Last Name = LIN

First Name = SHU-CHUN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09991341	Not Issued	71	11/16/2001	Method for applying a defect finder mark to a backend photomask making process	LIN, SHU-CHUN
10232975	6968338	150	08/29/2002	EXTENSIBLE DATABASE FRAMEWORK FOR MANAGEMENT OF UNSTRUCTURED AND SEMI-STRUCTURED DOCUMENTS	LIN, SHU-CHUN Y.
11250939	Not Issued	19	01/01/0001	Extensible database framework for management of unstructured and semi-structured documents	LIN, SHU-CHUN Y.
07426326	Not Issued	161	10/24/1989	ACETYLATION OF PHENOLIC COMPOUNDS	LIN, SHU-CHUNG
07714910	5136084	150	06/14/1991	PROCESS FOR THE ACETYLATION OF A MONO-SUBSTITUTED PHENOL AND A MONO-SUBSTITUTED NAPHTHOL	LIN, SHU-CHUNG

Inventor Search Completed: No Records to Display.

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Inventor Name Search Result

Your Search was:

Last Name = LIN

First Name = CHIN-HSIANG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09046309</u>	Not Issued	161	03/23/1998	NOVEL EXPOSURE METHOD TO INCREASE PHOTOLITHOGRAPHY RESOLUTION BY USING BI-LAYER RESIST FOR SEMICONDUCTOR DEVICES	LIN, CHIN-HSIANG
<u>09156056</u>	6215578	150	09/17/1998	ELECTRONICALLY SWITCHABLE OFF-AXIS ILLUMINATION BLADE FOR STEPPER ILLUMINATION SYSTEM	LIN, CHIN-HSIANG
<u>09156057</u>	6121626	150	09/17/1998	METHOD AND SYSTEM OF EXPOSURE WITH A UNIVERSAL DYNAMIC MASK AND CHARGE COUPLED DEVICE IMAGE FEEDBACK CONTROL	LIN, CHIN-HSIANG
<u>09224718</u>	6054347	150	01/04/1999	METHOD FOR FORMING A MODIFIED CROWN SHAPED, DYNAMIC RANDOM ACCESS MEMORY, (DRAM), CAPACITOR STRUCTURE	LIN, CHIN-HSIANG
<u>09238097</u>	Not Issued	161	01/27/1999	OVERLAY TARGET FOR SEMICONDUCTOR FABRICATION	LIN, CHIN-HSIANG
<u>09596900</u>	6428938	150	06/19/2000	PHASE-SHIFT MASK FOR PRINTING HIGH-RESOLUTION IMAGES AND A METHOD OF FABRICATION	LIN, CHIN-HSIANG
<u>09685430</u>	6365228	150	10/10/2000	Process for spin-on coating with an organic material having a low dielectric constant	LIN, CHIN-HSIANG
<u>09715657</u>	6677231	150	11/17/2000	METHOD FOR INCREASING ADHESION ABILITY OF	LIN, CHIN-HSIANG

				DIELECTRIC MATERIAL IN SEMICONDUCTOR	
<u>09801330</u>	<u>6429115</u>	150	03/07/2001	METHOD OF MANUFACTURING MULTILEVEL INTERCONNECTS INCLUDING PERFORMING A SURFACE TREATMENT TO FORM A HYDROPHILIC SURFACE LAYER	LIN, CHIN-HSIANG
<u>09908704</u>	Not Issued	161	07/20/2001	Method of manufacturing a semiconductor memory device with a gate dielectric stack	LIN, CHIN-HSIANG
<u>09920632</u>	Not Issued	161	08/03/2001	Method for forming polysilicon layer by using deuterium in semiconductor manufacturing process	LIN, CHIN-HSIANG
<u>09990397</u>	Not Issued	161	11/20/2001	Flash memory structure	LIN, CHIN-HSIANG
<u>09990862</u>	Not Issued	161	11/13/2001	Structure of a flash memory	LIN, CHIN-HSIANG
<u>09991341</u>	Not Issued	71	11/16/2001	Method for applying a defect finder mark to a backend photomask making process	LIN, CHIN-HSIANG
<u>10002978</u>	Not Issued	161	12/06/2001	Method for forming an ONO structure in one chamber	LIN, CHIN-HSIANG
<u>10003317</u>	<u>6638879</u>	150	12/06/2001	METHOD FOR FORMING NITRIDE SPACER BY USING ATOMIC LAYER DEPOSITION	LIN, CHIN-HSIANG
<u>10080323</u>	<u>6721939</u>	150	02/19/2002	ELECTRON BEAM SHOT LINEARITY MONITORING	LIN, CHIN-HSIANG
<u>10141614</u>	<u>6875624</u>	150	05/08/2002	COMBINED E-BEAM AND OPTICAL EXPOSURE SEMICONDUCTOR LITHOGRAPHY	LIN, CHIN-HSIANG
<u>10196974</u>	<u>6858353</u>	150	07/17/2002	INCREASED-CONTRAST FILM FOR HIGH-TRANSMITTANCE ATTENUATED PHASE-SHAFT MASKS	LIN, CHIN-HSIANG
<u>10212224</u>	Not Issued	161	08/06/2002	Method for forming a gate dielectric layer by a single wafer process	LIN, CHIN-HSIANG
<u>10236022</u>	<u>6713365</u>	150	09/04/2002	METHODS FOR FILLING SHALLOW TRENCH ISOLATIONS HAVING HIGH ASPECT RATIOS	LIN, CHIN-HSIANG

<u>10309427</u>	Not Issued	93	12/04/2002	NOVEL METHOD AND SYSTEMS TO PRINT CONTACT HOLE PATTERNS	LIN, CHIN-HSIANG
<u>10326499</u>	Not Issued	30	12/20/2002	Progressive self-learning defect review and classification method	LIN, CHIN-HSIANG
<u>10402590</u>	Not Issued	95	03/28/2003	PATTERN COMPENSATION FOR STITCHING	LIN, CHIN-HSIANG
<u>10606823</u>	Not Issued	41	06/26/2003	Multiple-exposure defect elimination	LIN, CHIN-HSIANG
<u>10636585</u>	Not Issued	161	08/08/2003	Method for planarizing semiconductor deposition layer	LIN, CHIN-HSIANG
<u>10661089</u>	Not Issued	41	09/10/2003	Method for mitigating chemical vapor deposition phosphorus doping oxide surface induced defects	LIN, CHIN-HSIANG
<u>10711015</u>	Not Issued	61	08/18/2004	Method of fabricating silicon carbide-capped copper damascene interconnect	LIN, CHIN-HSIANG
<u>10758450</u>	<u>6923645</u>	150	01/12/2004	GAS MIXING DEVICE	LIN, CHIN-HSIANG
<u>10765531</u>	Not Issued	30	01/27/2004	System and method for processing masks with oblique features	LIN, CHIN-HSIANG
<u>10792084</u>	Not Issued	30	03/03/2004	Holographic reticle and patterning method	LIN, CHIN-HSIANG
<u>10802087</u>	Not Issued	30	03/16/2004	Method and system for immersion lithography lens cleaning	LIN, CHIN-HSIANG
<u>10837528</u>	Not Issued	41	04/30/2004	Plasma assisted pre-planarization process	LIN, CHIN-HSIANG
<u>10842065</u>	Not Issued	30	05/10/2004	Method utilizing compensation features in semiconductor processing	LIN, CHIN-HSIANG
<u>10867076</u>	Not Issued	30	06/14/2004	Semiconductor device with scattering bars adjacent conductive lines	LIN, CHIN-HSIANG
<u>10910480</u>	Not Issued	30	08/03/2004	Megasonic immersion lithography exposure apparatus and method	LIN, CHIN-HSIANG
<u>10918413</u>	<u>6968251</u>	150	08/16/2004	DEFECT ANALYSIS SAMPLING CONTROL	LIN, CHIN-HSIANG
<u>10928455</u>	Not Issued	30	08/27/2004	Effectively water-free immersion lithography	LIN, CHIN-HSIANG
<u>10999625</u>	Not Issued	30	11/30/2004	Method for reducing wafer charging during drying	LIN, CHIN-HSIANG
<u>11009505</u>	Not Issued	30	12/10/2004	Immersion optical projection system	LIN, CHIN-HSIANG
<u>11029992</u>	Not	30	01/05/2005	Novel wafer repair method using	LIN, CHIN-

	Issued			direct-writing	HSIANG
11043304	Not Issued	30	01/26/2005	High resolution lithography system and method	LIN, CHIN-HSIANG
11044517	Not Issued	20	01/27/2005	Utilizing compensation features in photolithography for semiconductor device fabrication	LIN, CHIN-HSIANG
11046049	Not Issued	30	01/27/2005	Method of reducing number of particals on low-k material layer	LIN, CHIN-HSIANG
11050312	Not Issued	25	02/03/2005	System and method for photolithography in semiconductor manufacturing	LIN, CHIN-HSIANG
11080316	Not Issued	20	03/14/2005	Combined e-beam and optical exposure semiconductor lithography	LIN, CHIN-HSIANG
11084495	Not Issued	30	03/18/2005	Top patterned hardmask and method for patterning	LIN, CHIN-HSIANG
11085295	Not Issued	30	03/21/2005	Method and apparatus for planarizing gap-filling material	LIN, CHIN-HSIANG
11099966	Not Issued	30	04/06/2005	System and method for overlay measurement in semiconductor manufacturing	LIN, CHIN-HSIANG
11129975	Not Issued	20	05/16/2005	Immersion lithography apparatus and method	LIN, CHIN-HSIANG

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